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JC665 U.S. PTO

Patent  
Attorney's Docket No. 025219-259

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

UTILITY PATENT  
APPLICATION TRANSMITTAL LETTER



JC643 U.S. PTO

09/595893



**Box PATENT APPLICATION**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Enclosed for filing is the utility patent application of Jean-René LEQUEPEYS, Dominique NOGUET, Laurent OUVRY and Didier VARREAU for MULTI-MOK MODULATION/DEMODULATION TRANSMISSION PROCESS, CORRESPONDING TRANSMITTER AND RECEIVER.

Also enclosed are:

- ☒ 4 sheet(s) of ☒ formal ☐ informal drawing(s);
- ☒ a claim for foreign priority under 35 U.S.C. §§ 119 and/or 365 is ☐ hereby made to \_  
filed in \_ on \_;  
☒ in the declaration;
- ☒ a certified copy of the priority document;
- ☐ a General Authorization for Petitions for Extensions of Time and Payment of Fees;
- ☐ \_\_\_\_\_ statement(s) claiming small entity status;
- ☐ an Assignment document;
- ☐ an Information Disclosure Statement; and
- ☒ Other: Preliminary Amendment
- ☒ An ☐ executed ☒ unexecuted declaration of the inventor(s)  
☒ also is enclosed ☐ will follow.
- ☐ Please amend the specification by inserting before the first line the sentence --This application claims priority under 35 U.S.C. §§119 and/or 365 to \_ filed in \_ on \_; the entire content of which is hereby incorporated by reference.--
- ☐ A bibliographic data entry sheet is enclosed.



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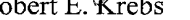
☒ The filing fee has been calculated as follows ☒ and in accordance with the enclosed preliminary amendment:

C L A I M S					
	NO. OF CLAIMS		EXTRA CLAIMS	RATE	FEE
Basic Application Fee					\$690.00 (101)
Total Claims	18	MINUS 20 =	0	x \$18.00 (103)	0.00
Independent Claims	1	MINUS 3 =	0	x \$78.00 (102)	0.00
If multiple dependent claims are presented, add \$260.00 (104)					0.00
Total Application Fee					690.00
If verified Statement claiming small entity status is enclosed, subtract 50% of Total Application Fee					0.00
Add Assignment Recording Fee \$40.00 (581) if Assignment document is enclosed					0.00
<b>TOTAL APPLICATION FEE DUE</b>					<b>\$ 690.00</b>

- ☒ This application is being filed without a filing fee. Issuance of a Notice to File Missing Parts of Application is respectfully requested.
- ☐ A check in the amount of \$ \_\_\_\_\_ is enclosed for the fee due.
- ☐ Charge \$ \_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.
- ☒ The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )  
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Jean-René LEQUEPEYS et al. ) Group Art Unit: Unassigned  
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Application No.: Unassigned ) Examiner: Unassigned  
 )  
Filed: Herewith )  
 )  
For: MULTI-MOK MODULATION/ )  
DEMODULATION TRANSMISSION )  
PROCESS, CORRESPONDING )  
TRANSMITTER AND RECEIVER )

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the subject application as follows:

**IN THE CLAIMS:**

Claim 5, line 4, delete "any of claims 1 to 4" and insert -- Claim 1 --.

Claim 6, line 8, delete "any of claims 1 to 5" and insert -- Claim 1 --.

Claim 7, line 13, delete "any of claims 1 to 5" and insert -- Claim 1 --.


**PLEASE ADD THE FOLLOWING CLAIMS:**

16. Process according to Claim 4, wherein the number of spread codes is the same  
in each group.

17. Process according to Claim 5, wherein the spread codes used are all different  
from one group to another and the code numbers are equal to powers of 2.

REMARKS

Respectfully submitted,

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Robert E. Krebs  
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Date: June 16, 2000

MULTI-MOK MODULATION/DEMODULATION TRANSMISSION PROCESS,  
CORRESPONDING TRANSMITTER AND RECEIVER

DESCRIPTION

FIELD OF THE INVENTION

The present invention relates to a multi-MOK modulation/demodulation transmission process, and the corresponding transmitter and receiver.

5       The invention is generally applied in digital applications and more particularly in wireless local area networks WLAN, wireless local loops WLL, mobile telephone communications, home automation and remote collection, communications in transportation, cabled  
10   television and multimedia services on cabled networks, etc.

STATE OF THE RELATED ART

15       The invention is based on the spectral spread technique. It is known that this technique consists of the modulation of a digital symbol to be transmitted by a pseudo-random sequence known by the user. Each sequence is composed of N items called "chips", the duration of which is one Nth of the duration of a  
20   symbol. This results in a signal with a spectrum spread over a range N times broader than that of the original signal. At reception, the demodulation consists of correlating the signal received with the sequence used at transmission to obtain the initial symbol.

25       This technique offers numerous advantages:

- discretion, since, as the power of the transmitted signal is constant and distributed over an N times broader band, its spectral power density is reduced by a factor N;

5       - immunity to voluntary or parasitic narrow band transmissions, the correlation operation carried out at the receiver resulting in the spectral spread of these transmissions;

10       - difficult interception (for usual signal to noise ratios), since the demodulation requires the knowledge of the sequence used at transmission;

15       - resistance to multiple paths which, under some conditions, induce selective frequency fading and, therefore, only affect the transmitted signal partially;

      - possibility of a code division multiple access or CDMA: several direct sequence spectral spread links can share the same frequency band using orthogonal spread codes.

20       However, this technique has one disadvantage: its low spectral efficiency. This term refers to the data bit rate and the width of the band occupied. If each data symbol contains m bits, the data bit rate is equal to m times the symbol rate, or mDs. The occupied band is equal to double the chip frequency, i.e. 2N times  
25       the symbol rate, or 2NDs. Therefore, this gives a spectral density equal to the ratio  $\frac{mDs}{2NDs}$ , or  $\frac{m}{2N}$ .

30       It would be possible to consider increasing the spectral efficiency by decreasing N, but this would affect the inherent qualities of the spread and, in particular, would have a detrimental effect on the

immunity of transmissions. It would also be possible to consider increasing the symbol rate, but interference between symbols would be worsened.

Another solution would consist of increasing  $m$ ,  
 5 the number of data bits per symbol, which would result in the use of complex, "higher order" modulations. Indeed, the present invention uses this method. Therefore, it is possible to summarise what these modulations consist of, essentially two of said  
 10 modulations, i.e. PSK or "Phase Shift Keying" modulation, which is a phase modulation (or encoding), and MOK or "M-ary Orthogonal Keying".

A description of these modulations can be found in two general works:

15 - Andrew J. VITERBI: "CDMA-Principles of Spread Spectrum Communication" Addison-Wesley Wireless Communications Series, 1975,

- John G. PROAKIS: "Digital Communication" McGraw-Hill International Editions, 3<sup>rd</sup> edition, 1995.

20 Firstly, phase shift keying modulation is most frequently a binary modulation, referred to as BPSK, or quaternary modulation, referred to as QPSK. In the first case, symbols can be encoded to one bit  $m=1$  and, in the second, to two bits  $m=2$ .

25 These modulations are generally used in their differential form DBPSK, DQPSK, which ensures satisfactory strength in difficult channels, when no phase retrieval loop is required. This differential form is also very well suited to processing the  
 30 diversity of propagation paths.



At reception, a differential demodulator performs the multiplication between the signal to be demodulated and its version delayed by a symbol period. In the case of quaternary modulation, two signal channels are used,  
 5 one channel which processes the component of the signal in phase with a carrier and another channel which processes the component in quadrature with the carrier.

Now, MOK modulation is a technique in which a signal taken from a set of orthogonal signals is  
 10 associated with each symbol to be transmitted. These signals may be spread codes from the same group of orthogonal codes. In this case, the modulation also carries out the spread. However, these signals may also not be perfectly orthogonal since the orthogonal strain  
 15 is lower than it seems. Naturally, however, performances are poorer in this case.

If a symbol is composed of  $m$  bits, there are  $2^m$  possible configurations for the different symbols. Therefore, the number  $M$  of codes available must be at  
 20 least equal to  $2^m$ . However, if the length of these codes is  $N$ , it is known that  $N$  orthogonal codes may be found. Therefore,  $M=N$  and the number of bits per symbol is limited to  $\log_2 N$ .

The MOK technique has a variant known as MBOK, M-  
 25 ary Bi-Orthogonal Keying, consisting of adding, to the set of orthogonal signals used in an MOK modulation, their opposites to form a set of  $2M$  signals, which are naturally no longer orthogonal with respect to each other. The demodulation also uses  $M$  correlators,  
 30 adapted to each of the  $M$  orthogonal codes, but also requires sign retrieval means.

With the MOK technique, if, to increase the spectral efficiency, the number  $m$  of bits in each symbol was increased by one unit, the number  $M$  of codes required would be doubled, which would multiply the number of receiver channels by 2. Therefore, the complexity increases at a higher rate than the spectral efficiency. Therefore, this technique involves certain limitations.

MOK and MBOK modulations are used in certain digital communication systems, in conjunction with a coherent reception structure, which requires the knowledge of the phase of the carrier. Sending a preamble, before the transmission of the useful data, is a conventional process enabling the estimation of this phase. However, in channels subject to fading and/or multiple paths, the carrier phase is subject to variations which may be rapid and must be detected and compensated by the reception system. This is generally obtained by the periodic transmission of preambles which then occupy the channel and induce a decrease in the useful data bit rate. According to this scheme, the times of the preamble and the useful data packet must be less than the channel coherence time (time during which the channel is considered to be stationary). In addition, the complexity of the reception structure is increased.

For these reasons, those skilled in the art prefer to use non-coherent, or differentially coherent, demodulation schemes, which do not require the knowledge of the phase data. These techniques do away with the use of long preambles, phase estimators and

phase derotators, in exchange for a slight loss of sensitivity. In addition, non-coherent demodulation simplifies the processing of the diversity of propagation paths very significantly since each path  
5 has, among other things, its own phase (and therefore would require its own phase estimator in a coherent scheme).

The present invention still aims to increase the spectral efficiency of links, while avoiding the  
10 disadvantages mentioned above and making use of the advantages of non-coherent demodulation.

#### DISCLOSURE OF THE INVENTION

To this end, the invention recommends to multiply  
15 the MOK modulation/demodulation operations to process a plurality of data blocks composed from the data to be transmitted. Naturally, this increases the number of codes, but, as will be seen more clearly below, this also increases the data bit rate very markedly. In the  
20 prior art, doubling, for example, the number of codes increased the number of bits transmitted by only one unit, while, in the invention, doubling the number of codes doubles the bit rate.

Therefore, more specifically, the invention  
25 relates to a data transmission process, characterised in that:

a) at transmission:

- the data to be transmitted is divided into N data blocks,
- 30 - these N blocks are processed in parallel in N M-ary orthogonal keying MOK modulation channels, each

modulation using a group of spread codes, each channel emitting a signal,

- all these signals are transmitted in series.

b) at reception:

5       - the signal received is processed in N M-ary orthogonal keying MOK demodulation channels, giving N data blocks,

- said N data blocks are grouped together in series to reproduce the transmitted data.

10       The MOK modulation and demodulation may consist of conventional type or M-ary bi-orthogonal keying MBOK type modulation and demodulation. They may also consist of an M-ary orthogonal keying modulation/demodulation combined with a phase shift keying PSK  
15 modulation/demodulation. The latter may be of the differential type, DPSK.

The number of spread codes may be the same in each group. It may also be different between groups. These numbers may advantageously be to the power of 2.

20       The present invention also relates to a transmitter for the implementation of the transmission phase of this process and a receiver for the implementation of the reception phase.

To underline the multiple nature of the process,  
25 and the link with the MOK techniques, the Applicant refers to its technique using the term multi-MOK, or M-MOK for short.

#### BRIEF DESCRIPTION OF THE FIGURES

30       - Figure 1 is a block diagram of a transmitter according to the invention;

- Figure 2 is a block diagram of a receiver according to the invention;

- Figure 3 is a diagram of a two-channel transmitter using the combined MOK-DPSK technique;

5       - Figure 4 is a diagram of a corresponding receiver;

- Figure 5 is a general diagram of a transmitter with N MOK-PSK type channels;

10       - Figure 6 is a general diagram of a corresponding receiver.

#### DESCRIPTION OF PARTICULAR EMBODIMENTS

Figure 1 is a schematic representation of a transmitter according to the invention. As shown, it  
 15       comprises a main input E receiving the data to be transmitted (generally symbols comprising one or more bits). It is assumed that a packet of this data comprises m bits. This data is divided into N blocks  $B_1, B_2, \dots, B_N$  by a serial/parallel converter type  
 20       circuit 2. These N blocks comprise  $m_1, m_2, \dots, m_N$  bits, respectively; these numbers may be equal, but not necessarily. The transmitter also comprises N MOK modulation means,  $4_1, 4_2, \dots, 4_N$ . Each of these means comprises a group of spread codes sufficient in number  
 25       to process the block it receives. As described in the description of the state of the related art, to process a block of  $m_i$  bits,  $2^{m_i}$  codes are required for a conventional MOK modulation. Since there are N groups of codes, the total number P of codes used in the  
 30       transmitter is:

$$P = \sum_{i=1}^N 2^{m_i}$$

If all the blocks are composed of the same number of bits, or  $m_u$ , and if no code is used several times, the transmitter uses  $P = N \cdot 2^{m_u}$  codes and it is capable  
 5 of processing  $N \times m_u$  bits.

Conversely, given the total number  $P$  of codes and the number  $N$  of groups, the number  $m_u$  of bits per block is:

$$m_u = \log_2(P/N)$$

10 and the total number of bits transmitted is:

$$m = \sum_u m_u, \text{ i.e. } N \log_2(P/N) \text{ if all the blocks have}$$

the same number of bits.

It is seen that to multiply the bit rate by  $N$ , it is sufficient to multiply the number of codes by  $N$ . If  
 15  $N=2$ , the bit rate is doubled by doubling the number of codes, while in the prior art doubling the number of codes only made it possible to add one bit. Therefore, the gain in terms of bit rate is considerable.

For the transmitter in Figure 1, the means  $4_1, 4_2, \dots, 4_N$  emit signals  $S_1, S_2, \dots, S_N$  (which are spread  
 20 codes selected according to the blocks to be transmitted). These signals are applied to a parallel-serial converter type circuit, the output of which is connected to transmission means 6.

25 Figure 2 represents a corresponding receiver. As shown, this receiver comprises reception means 10, which emit a signal  $R$ , which is processed in a group of  $P$  filters  $11_1, 11_2, \dots, 11_P$  which may be considered to be distributed in  $N$  groups of filters, these filters  
 30 being adapted to the spread code of the different

groups used at transmission. Each of these filters receives the signal  $R$  and emits a filtered signal  $R_1, R_2, \dots, R_P$ .

These filters are followed by the same number of means  $12_1, 12_2, \dots, 12_P$  to estimate the energy (or amplitude) of the filtered signals and a circuit 13 capable of determining, in each of the  $N$  groups of signals, which signal has the greatest energy (or amplitude). The circuit 13 has  $N$  outputs  $s_1, s_2, \dots, s_N$  each emitting the rank of the signal with the greatest energy. These outputs are connected to  $N$  code tables  $14_1, 14_2, \dots, 14_N$  which makes it possible to retrieve the  $N$  codes corresponding to these  $N$  ranks and emit the  $N$  corresponding data blocks  $B_1, B_2, \dots, B_N$ . The receiver is completed by a parallel-serial converter type circuit 15, which reproduces, on a main output  $S$ , the transmitted data (in  $m$  bits).

The present invention may be used within the scope of a particular PSK and MOK technique referred to as DP-MOK. This technique was the subject of the French patent application No. 98 11564 submitted on 16<sup>th</sup> September 1998 by the present Applicant, but which is not part of the related art to be taken into account for the consideration of the inventive activity of the present invention. In this DP-MOK technique, one part of the bits of each symbol is transmitted according to the MOK technique and another part according to the DPSK technique with spectral spread by the sequence selected for the MOK technique. At reception, the sequence used at transmission is first of all reproduced by adapted parallel filtering, thus

reproducing a part of the bits of the symbol. The appropriate filter signal is demodulated differentially to retrieve the other part of the bits.

Figure 3 shows an embodiment of a transmitter using this DP-MOK technique in the particular case where two channels are used ( $N=2$ ). The two channels comprise the same means indicated by references indexed 1 for the first and 2 for the second. Only the first will be described, since the second can be immediately deduced from it.

The data to be transmitted ( $m$  bits) is converted to parallel by a serial-parallel converter 18 which emits two blocks  $B_1$  and  $B_2$  with the same number of bits  $m_1$  and  $m_2$ . The first channel comprises:

- means 22<sub>1</sub> to divide the  $m$  bits of  $B_1$  into a first subgroup  $(23_{\text{MOK}})_1$  of  $(m_{\text{MOK}})_1$  bits and a second subgroup  $(23_{\text{DPSK}})_1$  of  $(m_{\text{DPSK}})_1$  bits where  $m_1 = (m_{\text{MOK}})_1 + (m_{\text{DPSK}})_1$ ;
- a conversion circuit 28<sub>1</sub> receiving the  $(m_{\text{MOK}})_1$  bits and converting them to an address directed towards a code table 30<sub>1</sub>, which comprises 2 to the power of  $(m_{\text{MOK}})_1$  orthogonal (or approximately orthogonal) spread codes and finally a generator 32<sub>1</sub> of the selected spread code  $(C_i)_1$ ;
- a differential encoding circuit 24<sub>1</sub>, essentially composed of a logic multiplier and a time-delay circuit;
- a PSK modulator referenced 26<sub>1</sub>;
- a spectral spread circuit 34<sub>1</sub> working with the code  $(C_i)_1$  emitted by the generator 32<sub>1</sub> and applying it to the modulated signal emitted by the modulator 26<sub>1</sub>.



The transmitter also comprises a radiofrequency stage 35 connected to the two channels and followed by a transmission antenna.

The corresponding receiver is represented in Figure 4. As shown, it comprises two identical channels, of which only the first will be described. This channel comprises:

- P filters  $(40_1)_1, (40_2)_1, \dots, (40_P)_1$  adapted to the P spread codes that can be used at transmission, these filters receiving the signal in the basic band;
- P samplers  $(42_1)_1, (42_2)_1, \dots, (42_P)_1$  controlled by a synchronisation signal;
- means 44<sub>1</sub> to determine the filtered signal which has the greatest energy (or amplitude) (maximum squared modulus), these means comprising a first output  $(44_1)_1$  carrying the number of the channel corresponding to the maximum signal, and a second output  $(44_2)_1$  emitting the actual signal;
- a circuit 46<sub>1</sub> connected to the first output and which, on the basis of the number of the channel corresponding to the signal with the greatest amplitude, emits the data  $(m_{MOK})_1$ ;
- a time-delayed multiplication circuit connected to the second output  $(44_2)_1$  and composed of a multiplier 52<sub>1</sub> of a circuit 54<sub>1</sub> inverting the phase and a time-delay circuit 56<sub>1</sub>;
- a PSK demodulator 58<sub>1</sub> emitting the data  $(m_{DPSK})_1$ ;
- the data  $(m_{MOK})_1$  and  $(m_{DPSK})_1$  then being grouped together to reconstitute the transmitted symbol  $S_1$ .

The receiver is completed by a parallel-serial converter 60 which groups together the signals  $S_1$  and  $S_2$  and reproduces the  $m$  bits transmitted.

The maximum signal selection operation takes some  
 5 time. As this operation is used to direct one of the inputs to the output (channel switching), it is essential to time-delay the channels by a corresponding time, since the switching must be carried out exactly on the data used for channel selection. Such time-delay  
 10 operations are standard in such techniques and are not shown.

Figures 3 and 4 are analytical, to give a clearer understanding of this particular embodiment. However, in practice, the circuits may be more synthetic as  
 15 shown in Figures 5 and 6. These Figures relate to a PSK technique.

Figure 5 shows a transmitter with an input E, a serial/parallel converter 70, a circuit 8 dividing into  $N$  MOK blocks and  $N$  PSK blocks, a table 90 of  $P$  codes  
 20 receiving on  $N$  inputs the  $N$  MOK blocks, a circuit 100 receiving the  $N$  codes selected in the table 90 and spreading the  $N$  PSK blocks, a circuit 110 combining the  $N$  spread signals  $S_1, \dots, S_N$ , and finally transmission means 112.

25 Figure 6 shows a receiver with reception means 118, a group of  $P$  adapted filters  $120_1, \dots, 120_P$ , a group of  $P$  energy estimation circuits  $130_1, \dots, 130_P$  for the filtered signals, a circuit 140 receiving the  $P$  estimations and emitting on  $N$  outputs the  $N$  numbers of  
 30 the channels carrying the  $N$  signals with the greatest energy,  $N$  circuits  $150_1, \dots, 150_N$  capable of selecting

the channel corresponding to the number addressed to it,  $N$  PSK demodulators  $152_1, \dots, 152_N$ , a formatting circuit 160 receiving on  $N$  first inputs  $N$  MOK blocks and on  $N$  second inputs  $N$  PSK blocks, this circuit emitting, on a main output  $S$ , the transmitted data.

Finally, it is possible to calculate the data bit rate obtained according to the invention for some particular cases.

In a first series of examples, it is assumed that all the codes are different.  $P$  is the total number of codes available. For the  $N$  codes used simultaneously to be different, at transmission, a code is selected from a group of  $P/N$  codes.

The number of MOK bits for each channel is:

$$(m_{\text{MOK}})_u = \log_2 P/N$$

$(m_{\text{PSK}})_u$  is the number of PSK bits on each channel.

The number of bits transmitted per symbol is:

$$m = m_{\text{MOK}} + m_{\text{PSK}} = N\{\log_2 P/N + (m_{\text{PSK}})_u\}$$

Table 1 gives some numeric examples of MOK ( $N=1$ ) (prior art) and M-MOK ( $N>1$ ) (invention where  $m_{\text{PSK}}=2$  (QPSK)) modulations.

	$P=8$	$P=16$
$N=1$ (MOK)	$m_{\text{MOK}}=3$ <b><math>m=5</math> bits/symb</b>	$m_{\text{MOK}}=4$ <b><math>m=6</math> bits/symb</b>
$N=2$ (M-MOK)	$m_{\text{MOK}}=2$ <b><math>m=8</math> bits/symb</b>	$m_{\text{MOK}}=3$ <b><math>m=10</math> bits/symb</b>
$N=4$ (M-MOK)	$m_{\text{MOK}}=1$ <b><math>m=12</math> bits/symb</b>	$m_{\text{MOK}}=2$ <b><math>m=16</math> bits/symb</b>
$N=8$ (M-MOK)	---	$m_{\text{MOK}}=1$ <b><math>m=24</math> bits/symb</b>

In the above examples, it was assumed that all the codes were different. However, it is possible to reuse

the same code. A special code must then be associated with each code so that the codes transmitted on the channel are always different. Therefore, it is necessary to have P codes plus one special code. For  
 5 simplification purposes, the value N=2 is taken.

The channels used are then selected from P and not from P/2. Consequently, the number of transmitted MOK bits is:

$$m_{\text{MOK}} = 2 \cdot \log_2 P \text{ instead of } 2 \cdot (\log_2) - 1$$

10 When the detector detects a data item on the channel of the special code, it knows that the MOK data item is identical on the two channels used.

Numeric examples in the QPSK case:

	P=8,	N=2:	$m = 2(3+2) = \mathbf{10 \text{ bits/symb}}$
15	P=16,	N=2:	$m = 2(4+2) = \mathbf{12 \text{ bits/symb}}$

CLAIMS

1. Data transmission process, characterised in that:

a) at transmission:

- the data to be transmitted is divided into N  
5 data blocks ( $B_1, B_2, \dots, B_N$ ),

- these N blocks are processed in parallel in N M-ary orthogonal keying (MOK) modulation channels, each modulation using a group of spread codes, each channel emitting a signal ( $S_1, S_2, \dots, S_N$ ),

10 - all these signals (S) are transmitted in series.

b) at reception:

- the signal received (R) is processed in N M-ary orthogonal keying (MOK) demodulation channels, giving N data blocks ( $B_1, B_2, \dots, B_N$ ),

15 - said N data blocks are grouped together in series to reproduce the transmitted data.

2. Process according to claim 1, wherein the modulation and demodulation consist of M-ary bi-  
20 orthogonal keying (MBOK) modulation and demodulation.

3. Process according to claim 1, wherein the modulation and demodulation consist of M-ary orthogonal keying (MOK) modulation and demodulation combined with  
25 phase shift keying (PSK) modulation and demodulation.

4. Process according to claim 3, wherein the phase shift keying modulation and demodulation consist of

differential phase shift keying (DPSK) modulation and demodulation.

5. Process according to any of claims 1 to 4,  
5 wherein the number of spread codes is the same in each group.

6. Process according any of claims 1 to 5, wherein  
10 the spread codes used are all different from one group to another and the code numbers are equal to powers of 2.

7. Process according to any of claims 1 to 5,  
15 wherein certain spread codes are used in several groups.

8. Transmitter for the implementation of the  
transmission phase of the process according to claim 1,  
characterised in that it comprises:

20 - means to divide the data to be transmitted into N data blocks ( $B_1, B_2, \dots, B_N$ ),

- means to process these N blocks in parallel in N  
M-ary orthogonal keying (MOK) modulation channels, each  
modulation using a group of spread codes, each channel  
25 emitting a signal ( $S_1, S_2, \dots, S_N$ ),

- means to transmit these N signals in series.

9. Transmitter according to claim 8, wherein the  
modulation is an M-ary bi-orthogonal keying (MBOK)  
30 modulation.

10. Transmitter according to claim 8, wherein the modulation is an M-ary orthogonal keying (MOK) modulation combined with a phase shift keying (PSK) modulation.

5

11. Transmitter according to claim 10, wherein the phase shift keying modulation is a differential phase shift keying (DPSK) modulation.

10 12. Receiver for the implementation of the reception phase of the process according to claim 1, characterised in that it comprises:

- means to process the signal received (R) in parallel in N M-ary orthogonal keying (MOK) demodulation channels, giving N data blocks ( $B_1$ ,  $B_2$ , ...,  $B_N$ ),

- means to group these N data blocks together in series and reproduce the transmitted data.

20 13. Receiver according to claim 12, wherein the demodulation is an M-ary bi-orthogonal keying (MBOK) demodulation.

25 14. Receiver according to claim 12, wherein the demodulation is an M-ary orthogonal keying (MOK) demodulation combined with a phase shift keying (PSK) demodulation.

30 15. Receiver according to claim 14, wherein the phase shift keying demodulation is a differential phase shift keying (DPSK) demodulation.

ABSTRACT OF THE DISCLOSURE

MULTI-MOK MODULATION/DEMODULATION TRANSMISSION PROCESS,  
CORRESPONDING TRANSMITTER AND RECEIVER

At transmission, the data to be transmitted is divided into N data blocks, these N blocks are processed in parallel in N M-ary orthogonal keying (MOK) modulation channels, each modulation using a group of spread codes, each channel emitting a signal, all these signals (S) are transmitted in series. At reception, the signal received (R) is processed in N M-ary orthogonal keying (MOK) demodulation channels and the blocks obtained are grouped together in series.

Applied to digital communications.

Fig. 1 and 2.



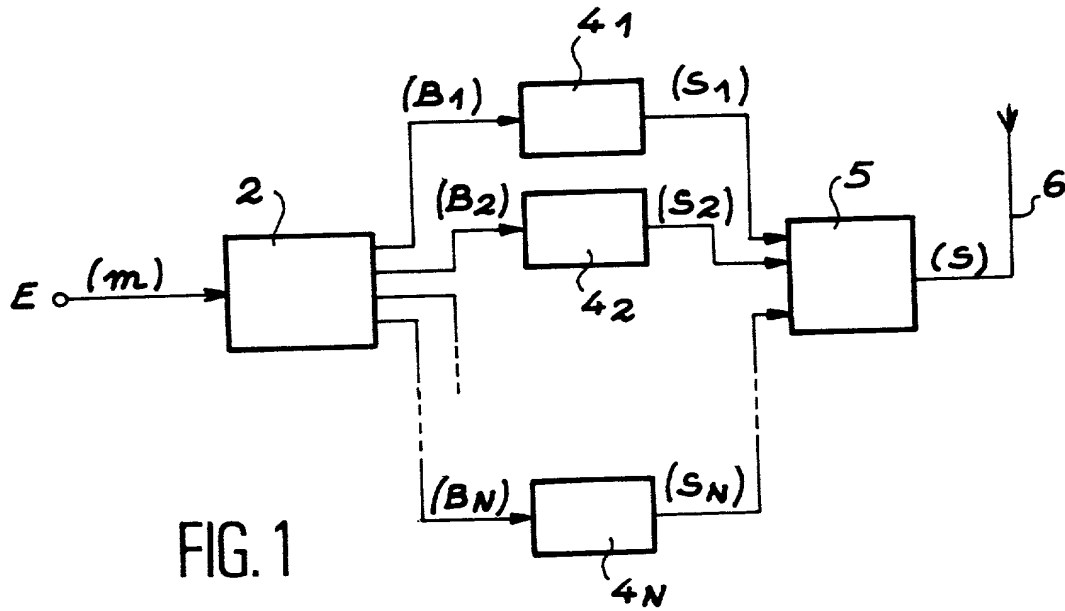


FIG. 1

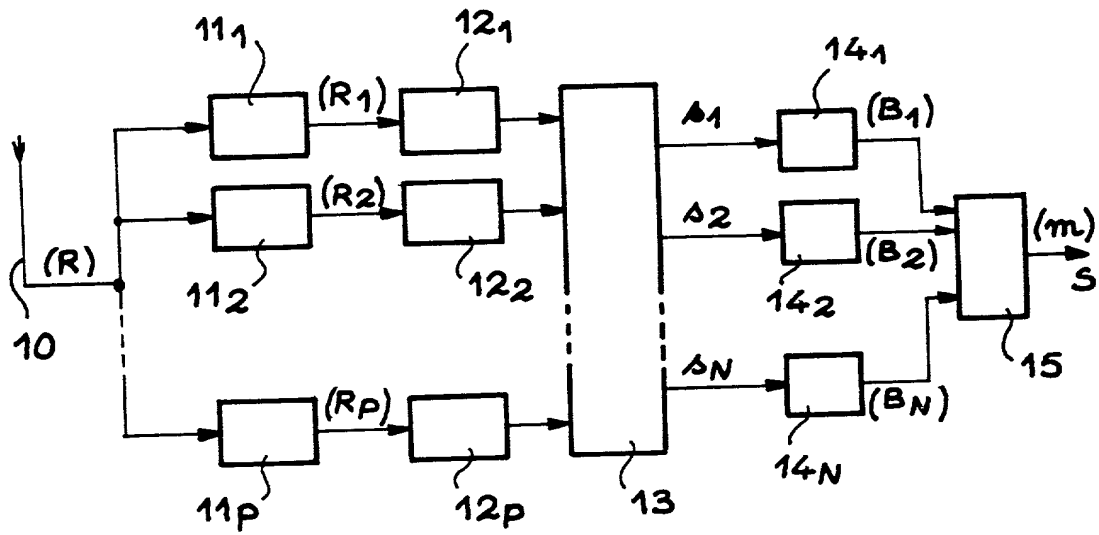


FIG. 2

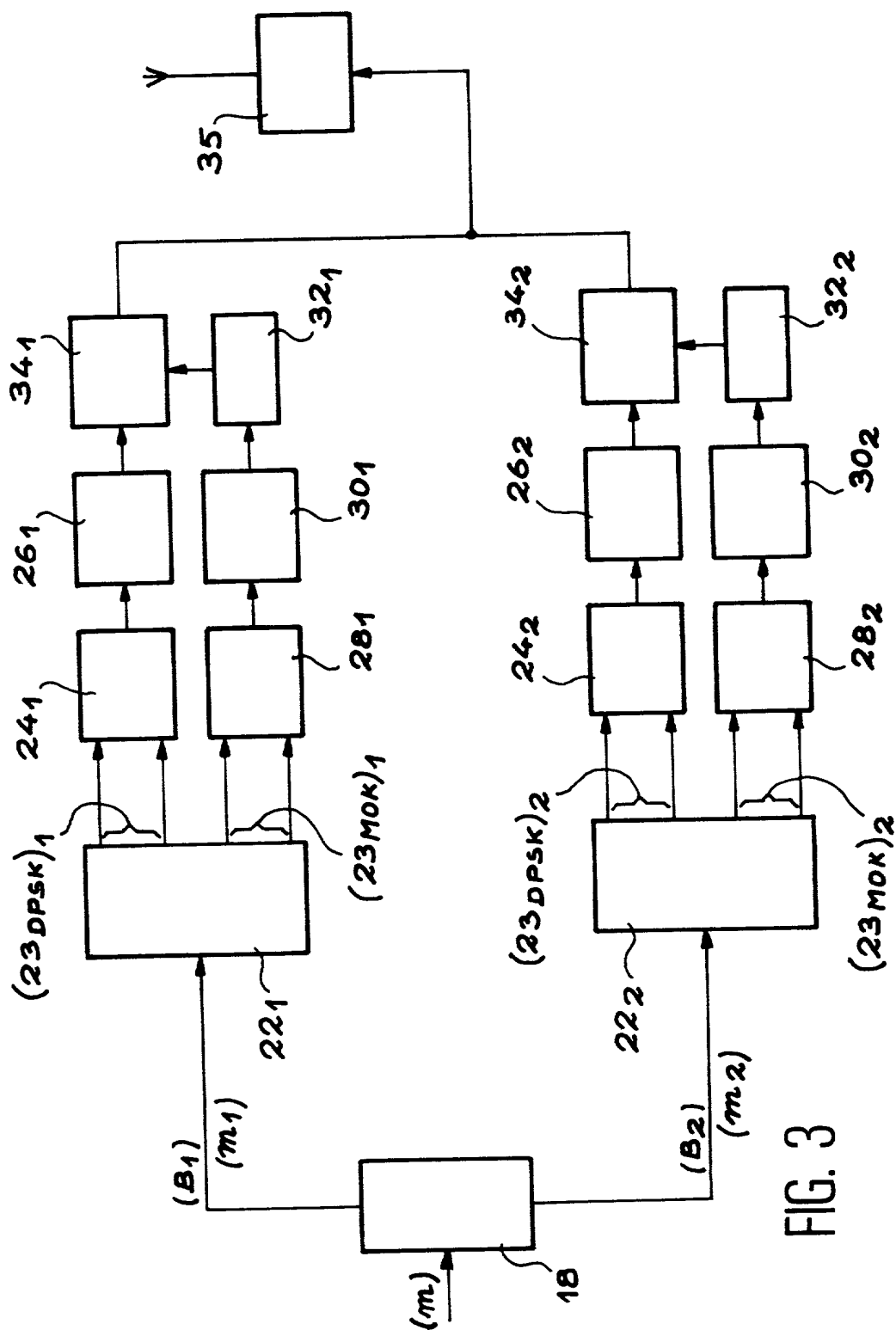


FIG. 3

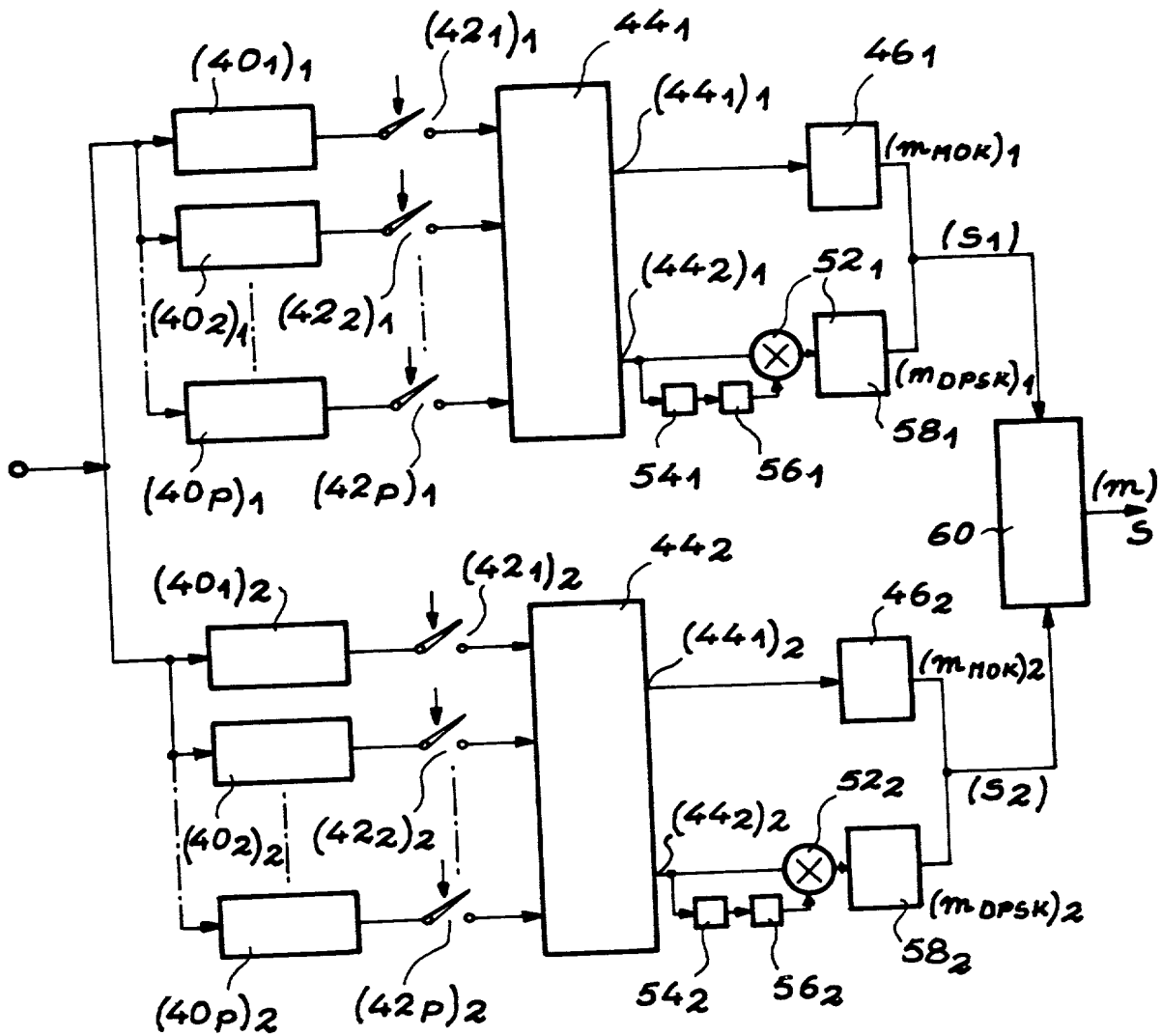


FIG. 4

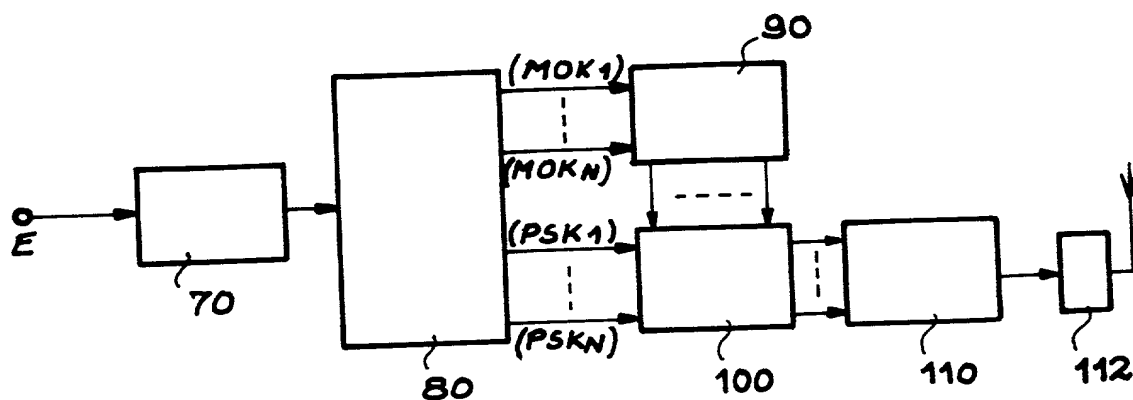


FIG. 5

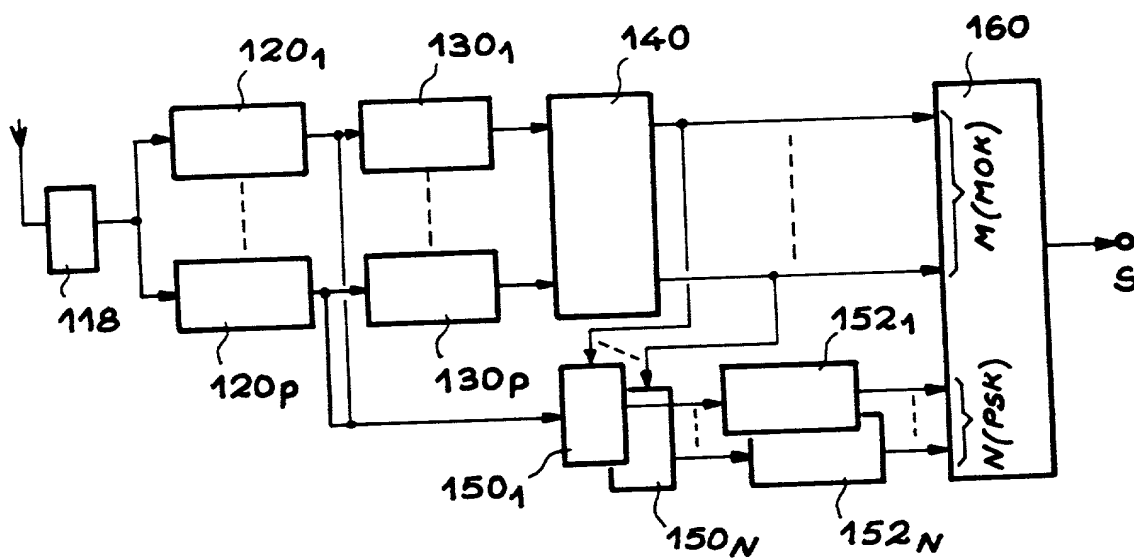


FIG. 6

**COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR UTILITY PATENT APPLICATION**

Attorney's Docket No.

025219-259

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I BELIEVE I AM THE ORIGINAL, FIRST AND SOLE INVENTOR (if only one name is listed below) OR AN ORIGINAL, FIRST AND JOINT INVENTOR (if more than one name is listed below) OF THE SUBJECT MATTER WHICH IS CLAIMED AND FOR WHICH A PATENT IS SOUGHT ON THE INVENTION ENTITLED:

MULTI-MOK MODULATION/DEMODULATION TRANSMISSION PROCESS, CORRESPONDING

TRANSMITTER AND RECEIVER

the specification of which

(check one)

☒ is attached hereto;

☐ was filed on \_\_\_\_\_ as

Application No. \_\_\_\_\_

and was amended on \_\_\_\_\_ ;  
(if applicable)

I HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE;

I ACKNOWLEDGE THE DUTY TO DISCLOSE TO THE OFFICE ALL INFORMATION KNOWN TO ME TO BE MATERIAL TO PATENTABILITY AS DEFINED IN TITLE 37, CODE OF FEDERAL REGULATIONS, Sec. 1.56 (as amended effective March 16, 1992);

I do not know and do not believe the said invention was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to said application; that said invention was not in public use or on sale in the United States of America more than one year prior to said application; that said invention has not been patented or made the subject of an inventor's certificate issued before the date of said application in any country foreign to the United States of America on any application filed by me or my legal representatives or assigns more than twelve months prior to said application;

I hereby claim foreign priority benefits under Title 35, United States Code Sec. 119 and/or Sec. 365 of any foreign application(s) for patent or inventor's certificate as indicated below and have also identified below any foreign application for patent or inventor's certificate on this invention having a filing date before that of the application(s) on which priority is claimed:

**COMBINED DECLARATION AND POWER OF ATTORNEY**

Attorney's Docket No.

025219-259

COUNTRY/INTERNATIONAL	APPLICATION NUMBER	DATE OF FILING (day, month, year)	PRIORITY CLAIMED
FRANCE	99 08308	29, June, 1999	YES <u>X</u> NO <u>  </u>
			YES <u>  </u> NO <u>  </u>

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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